

Day: Thursday Date: 1/5/2006 Time: 16:55:34

Inventor Name Search Result

Your Search was:

Last Name = LEE First Name = ERIC

-					
Application#	Patent#	Status	Date Filed	Title	Inventor Name
05917775	D259064	150	06/21/1978	SOAP BAR OR SIMILAR ARTICLE	LEE, ERIC
05944473	D257223	150	09/21/1978	DECANTER	LEE, ERIC
05944475	D257224	150	09/21/1978	DECANTER	LEE, ERIC
07422442	Not Issued	164	10/17/1989	FRONT PANEL FOR A COMPUTER	LEE, ERIC
<u>07426853</u>	Not Issued	164	10/26/1989	COMPUTER PANEL	LEE, ERIC
<u>07526731</u>	Not Issued	161	05/22/1990	A DIGITAL CONTROL DEVICE ON A SIMULATION KEYBOARD	LEE, ERIC
<u>07727436</u>	Not Issued	161	07/09/1991	TOOTHBRUSH WITH TOOTHPASTE FEEDER	LEE, ERIC
<u>07759089</u>	Not Issued	161	09/13/1991	CUTTING TOOL	LEE, ERIC
08657739	<u>5742443</u>	150	05/31/1996	METHOD AND APPARATUS FOR DATA PLACEMENT OF CONTINUOUS MEDIA TO UTILIZE BANDWIDTH EFFICIENCY	LEE, ERIC
08780578	Not Issued	161	01/08/1997	SURGICAL ACCESS DEVICE	LEE, ERIC
08780982	5814059	150	01/08/1997	VEIN-BRANCH ACCESSING DEVICE	LEE, ERIC
08781273	<u>6210370</u>	150	01/10/1997	ACCESS DEVICE WITH EXPANDABLE CONTAINMENT MEMBER	LEE, ERIC
08814746	5848992	150	03/07/1997	SUPERFASCIAL SURGICAL ACCESS DEVICE	LEE, ERIC
08897886	Not Issued	161	07/22/1997	SURGICAL ACCESS DEVICE WITH REMOVABLE TUBE FOR CLEANING	LEE, ERIC
<u>09037446</u>	6070109	150	03/10/1998	ROBOT CALIBRATION SYSTEM	LEE, ERIC
<u>09353413</u>	6267773	150	07/15/1999	LOW PROFILE SURGICAL CLIP	LEE, ERIC
09452269	Not Issued	18	12/01/1999	METHOD AND APPARATUS USING LAMINATED FOILS FOR PLACING CONDUCTIVE PREFORMS	LEE, ERIC
09570857	6540735	150	05/12/2000	SYSTEM AND METHOD FOR FACILITATING HEMOSTASIS OF BLOOD VESSEL PUNCTURES WITH ABSORBABLE SPONGE	LEE, ERIC
09621670	Not Issued	71	07/24/2000	DEPTH AND PUNCTURE CONTROL FOR SYSTEM FOR HEMOSTASIS OF BLOOD VESSEL	LEE, ERIC
09772115	6765269	150	01/26/2001	CONFORMAL SURFACE SILICIDE STRAP ON SPACER AND METHOD OF MAKING SAME	LEE, ERIC
09859682	6984219	150	05/18/2001	DEPTH AND PUNCTURE CONTROL FOR BLOOD VESSEL HEMOSTASIS SYSTEM	LEE, ERIC
<u>09876620</u>	<u>6516248</u>	150	06/07/2001	ROBOT CALIBRATION SYSTEM AND METHOD OF DETERMINING A POSITION OF A ROBOT RELATIVE TO AN ELECTRICALLY-CHARGED CALIBRATION OBJECT	LEE, ERIC
09905488	6453683	150	07/13/2001	TAPERED SLOT CRYOPRESERVATION SYSTEM WITH CONTROLLED DENDRITIC FREEZING FRONT VELOCITY	LEE, ERIC
10036234	Not Issued	لا	12/24/2001	Encoding method and system for reducing inter-symbol interference effects in transmission over a serial link	LEE, ERIC
10095422	Not Issued	71	03/12/2002	Method and system for reducing inter-symbol interference effects in transmission over a serial link with mapping of each word in a cluster of received words to a single transmitted word	LEE, ERIC
10125770	Not Issued	41	04/18/2002	Methods and compositions relating to modulation of A20	LEE, ERIC

10171860	Not Issued	41	06/14/2002	Method and apparatus for regenerating a clock for auxiliary data transmitted over a serial link with video data	LEE, ERIC
10192296	6914637	150	07//10/2002	METHOD AND SYSTEM FOR VIDEO AND AUXILIARY DATA TRANSMISSION OVER A SERIAL LINK	LEE, ERIC
10366752	<u>6964658</u>	150	02/14/2003	SYSTEM AND METHOD FOR FACILITATING HEMOSTASIS OF BLOOD VESSEL PUNCTURES WITH ABSORBABLE SPONGE	LEE, ERIC
<u>10395370</u>	Not Issued	161	03/24/2003	Thermal barrier for managing heat transfer	LEE, ERIC
10439446	Not Issued	93	05/16/2003	METHOD AND CIRCUIT FOR DC MEASUREMENT OF PHASE UNIFORMITY OF MULTI-PHASE CLOCKS	LEE, ERIC
10439722	Not Issued	41	05/16/2003	Mobile electronic device with tactile keyboard	LEE, ERIC
10692549	Not Issued	30	10/23/2003	System and method for taxonomy branch matching	LEE, ERIC
10781405	Not Issued	41	02/18/2004	Cable with circuitry for asserting stored cable data or other information to an external device or user	LEE, ERIC
10986636	Not Issued	30	11/12/2004	Process for forming CMOS devices using removable spacers	LEE, ERIC
11133651	Not Issued	20	05/19/2005	Phacoemulsification tip	LEE, ERIC
11232176	Not Issued	30	09/21/2005	Surgical apparatus	LEE, ERIC
<u>11260701</u>	Not Issued	30	10/27/2005	Surgical method	LEE, ERIC
29055988	<u>D398226</u>	150	06/19/1996	TOY PACKAGING	LEE, ERIC
29113816	D435220	150	11/15/1999	JAR	LEE, ERIC
60198682	Not Issued	159	04/20/2000	Method and apparatus for total media television	LEE, ERIC
60244074	Not Issued	159	10/26/2000	System and method for digital music file transfer	LEE, ERIC
60285524	Not Issued	159	04/20/2001	Integrated identity and access management system	LEE, ERIC
60296655	Not Issued	159	06/06/2001	Method and apparatus for adaptive control of PLL loop bandwidth	LEE, ERIC
60334622	Not Issued	159	11/01/2001	Systems and methods for freezing and storing biopharmaceutical material	LEE, ERIC
<u>60464770</u>	Not Issued	159	04/23/2003	Mobile gas to liquids plant using fischer-tropsch synthesis	LEE, ERIC
60675947	Not Issued	20	04/28/2005	Autologous somatic cells from peripheral blood and uses thereof	LEE, ERIC
60711506	Not Issued	20	08/26/2005	Teaching abacus for encouraging positive behavior	LEE, ERIC
60741493	Not Issued	20	11/30/2005	Subaccounts enhancement	LEE, ERIC
08844393	Not Issued	161	04/18/1997	APPARATUS AND METHOD FOR PROCESSING TEXTILE PRODUCTS WITH LOW LIQUOR -TO PRODUCT RATIO	LEE, ERIC C S

Search and Display More Records.

	Last Name	First Name	
Search Another: Inventor	lee	eric	Search

To go back use Back button on your browser toolbar.



Day : Thursday Date: 1/5/2006 Time: 16:55:06

Inventor Name Search Result

Your Search was:

Last Name = KIM First Name = BRUCE

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09126909	6111414	150	07/31/1998	SYSTEM, CIRCUIT, AND METHOD FOR TESTING AN INTERCONNECT IN A MULTI-CHIP SUBSTRATE	KIM, BRUCE
09579811	6870930	150	05/26/2000	METHODS AND SYSTEMS FOR TMDS ENCRYPTION	KIM, BRUCE
09905511	Not Issued	71	07/13/2001	Method and apparatus for adaptive control of PLL loop bandwidth	KIM, BRUCE
10099533	Not Issued	71	03/15/2002	Combining a clock signal and a data signal	KIM, BRUCE
10623821	Not Issued	20	07/21/2003	Electrode for implant in live tissue with flexible region to accommodate micro-movement	KIM, BRUCE
10730104	Not Issued	19	12/09/2003	Adjustable shutter	KIM, BRUCE
10763905	Not Issued	30	01/23/2004	Reduced dead-cycle, adaptive phase tracking method and apparatus	KIM, BRUCE
10842231	Not Issued	30	05/10/2004	Data sampling method and apparatus using through-transition counts to reject worst sampling position	KIM, BRUCE
10858166	Not Issued	41	06/01/2004	Apparatus for detecting defect in circuit pattern and defect detecting system having the same	KIM, BRUCE
60117488	Not Issued	159	01/27/1999	ENCRYPTION METHOD FOR TMDS	KIM, BRUCE
60136695	Not Issued	159	05/28/1999	ENCRYPTION METHOD FOR TMDS	KIM, BRUCE
60276672	Not Issued	159	03/16/2001	Encoding scheme for a phase modulated clock signal	KIM, BRUCE
60277219	Not Issued	159	03/19/2001	Encoding scheme for a phase modulated clock signal	KIM, BRUCE
60296655	Not Issued	159	06/06/2001	Method and apparatus for adaptive control of PLL loop bandwidth	KIM, BRUCE
<u>60431881</u>	Not Issued	159	12/09/2002	Adjustable louver and panel	KIM, BRUCE
60434357	Not Issued	159	12/17/2002	Implantable electrodes with flexible regions to accommodate micromovement	KIM, BRUCE
60475032	Not Issued	159	06/02/2003	Data sampling method and apparatus using through-transition counts to reject worst sampling position	KIM, BRUCE
<u>10217827</u>	Not Issued	30	08/12/2002	Low-power RF receiver on single SOI substrate	KIM, BRUCE C.
60002021	Not Issued	159	08/08/1995	MCM INTERCONNECT TEST	KIM, BRUCE C.
60022100	Not Issued	159	07/17/1996	MCM INTERCONNECT TEST	KIM, BRUCE C.
60054288	Not Issued	159	07/31/1997	MCM CONNECT TEST	KIM, BRUCE C.
60311520	Not Issued	159	08/10/2001	Low-power RF receiver on SOI	KIM, BRUCE C.
08127824	<u>5397976</u>	150	09/28/1993	CONTROL SYSTEM FOR VOLTAGE BILATERAL CURRENT SOURCE	KIMBALL, BRUCE
<u>06135850</u>	Not Issued	163	03/31/1980	CARBON DIOXIDE ANALYZER FOR GREENHOUSES	KIMBALL, BRUCE A.
07655060	5029353	150	02/14/1991	FITTED BED SHEET WITH HIGHLY ELASTICIZED CORNER AND MATTRESS-RETENTION POCKET	KIMBALL, BRUCE H.
07771637	Not Issued	166	10/04/1991	FITTED MATTRESS COVER WITH HIGHLY STRETCHABLE CORNER	KIMBALL, BRUCE H.
08004636	Not Issued	161	01/12/1993	FITTED MATTRESS COVER WITH HIGHLY STRETCHABLE CORNER	KIMBALL, BRUCE H.
<u>10794926</u>	Not Issued	30	03/05/2004	Deployment processes for new technology systems	KIMBLE, BRUCE
60530092	Not Issued	159	12/15/2003	Initial deployment of MIMO systems	KIMBLE, BRUCE
10666480	<u>6919426</u>	150	09/18/2003	PEPTIDES AND RELATED MOLECULES THAT MODULATE NERVE GROWTH FACTOR ACTIVITY	KIMMEL, BRUCE

410					
11127702	Not Issued	30	05/11/2005	Peptides and related molecules that modulate nerve growth factor activity	KIMMEL, BRUCE
60412524	Not Issued	159	09/19/2002	Peptides and related molecules that modulate nerve growth factor activity	KIMMEL, BRUCE
60438807	Not Issued	159	01/07/2003	Methods for modifying protein sequences	KIMMEL, BRUCE
<u>60450573</u>	Not Issued	159	02/26/2003	p185Her2 receptor binding proteins	KIMMEL, BRUCE
60500731	Not Issued	159	09/04/2003	Thermal tolerant receptor binding proteins	KIMMEL, BRUCE
60639827	Not Issued	159	12/27/2004	Orally deliverable antibodies	KIMMEL, BRUCE
60654018	Not Issued	20	02/17/2005	Novel antigen-binding polypeptides and their uses	KIMMEL, BRUCE
60660886	Not Issued	20	03/11/2005	Human interferon molecules and their uses	KIMMEL, BRUCE
<u>11155909</u>	Not Issued	19	06/17/2005	Novel antigen-binding polypeptides and their uses	KIMMEL, BRUCE E.
11187687	Not Issued	30	07/21/2005	Biosynthetic polypeptides utilizing non-naturally encoded amino acids	KIMMEL, BRUCE E.
60683553	Not Issued	20	05/20/2005	Novel prodrug containing molecule compositions and their uses	KIMMEL, BRUCE E.
<u>60687173</u>	Not Issued	20	06/03/2005	Human interferon molecules and their uses	KIMMEL, BRUCE E.
60733339	Not Issued	20	11/02/2005	Biosynthetic polypeptide fusion inhibitors	KIMMEL, BRUCE E.
08436074	5753438	150	05/08/1995	METHOD TO DIAGNOSE HEREDITARY HEMOCHROMA TOSIS	KIMMEL, BRUCE E.
08438567	5670321	150	05/10/1995	EFFICIENT METHOD TO CONDUCT LARGE-SCALE GENOME SEQUENCING	KIMMEL, BRUCE E.
08599252	<u>5705343</u>	150	02/09/1996	METHOD TO DIAGNOSE HEREDITARY HEMOCHROMA TOSIS	KIMMEL, BRUCE E.
08149462	5410854	150	11/09/1993	CONNECTOR BRACKETS	KIMMELL, BRUCE A.

Inventor Search Completed: No Records to Display.

	Last Name	First Name	
Search Another: Inventor	kim	ruce	Search

To go back use Back button on your browser toolbar.

Back to $\ \underline{PALM}\ |\ \underline{ASSIGNMENT}\ |\ \underline{OASIS}\ |\ Home\ page$



Day : Thursday Date: 1/5/2006 Time: 16:54:50

Inventor Name Search Result

Your Search was:

Last Name = KIM First Name = OOK

Application#	Patent#	Status	Date Filed	Title	Inventor Name
07669534	5200828	250	03/14/1991	AUTOFOCUSING DEVICE FOR USE IN A VIDEO CAMERA AND AN AUTOFOCUSING METHOD THEREOF	KIM, OOK
07992695	Not Issued	161	12/18/1992	AUTOFOCUSING DEVICE FOR USE IN A VIDEO CAMERA AND AN AUTOFOCUSING METHOD THEREOF	KIM, OOK
08359204	<u>5612696</u>	150	12/19/1994	DIGITAL-TO-ANALOG CONVERTER OF CURRENT SEGMENTATION	KIM, OOK
<u>08747767</u>	Not Issued	161	11/13/1996	INDUCTOR USING DEPLETION REGION	KIM, OOK
08769808	<u>5880616</u>	150	12/18/1996	DIGITAL LOGIC LEVEL CONVERSION CIRCUIT WITH A SMALL SINUSOIDAL WAVE INPUT	KIM, OOK
08877408	<u>5886550</u>	150	06/16/1997	INTEGRATED CIRCUIT BUILT-IN TYPE SUPPLY POWER DELAY CIRCUIT	KIM, OOK
08892938	<u>5818306</u>	150	07/15/1997	VOLTAGE CONTROL OSCILLATION CIRCUIT USING CMOS	KIM, OOK
08893459	6011425	150	07/11/1997	CMOS OFFSET TRIMMING CIRCUIT AND OFFSET GENERATION CIRCUIT	KIM, OOK
08970350	6121818	150	11/14/1997	MIXER USING REPLICA VOLTAGE-CURRENT CONVERTER	KIM, OOK
09099388	6091289	150	06/18/1998	LOW PASS FILTER	KIM, OOK
09124142	6069537	150	07/29/1998	DOUBLE MODE MODULATOR	KIM, OOK
09454389	6400932	150	12/03/1999	LOW OFFSET AUTOMATIC FREQUENCY TUNING CIRCUITS FOR CONTINUOUS-TIME FILTER	KIM, OOK
09905511	Not Issued	71	07/13/2001	Method and apparatus for adaptive control of PLL loop bandwidth	KIM, OOK
09989587	6717478	150	11/20/2001	MULTI-PHASE VOLTAGE CONTROLLED OSCILLATOR (VCO) WITH COMMON MODE CONTROL	KIM, OOK
09989590	<u>6845461</u>	150	11/20/2001	HIGH-SPEED BUS WITH EMBEDDED CLOCK SIGNALS	KIM, OOK
09989645	6809567	150	11/20/2001	SYSTEM AND METHOD FOR MULTIPLE-PHASE CLOCK GENERATION	KIM, OOK
10043886	6693985	150	10/26/2001	CLOCK AND DATA RECOVERY METHOD AND APPARATUS	KIM, OOK
10099533	Not Issued	71	03/15/2002	Combining a clock signal and a data signal	KIM, OOK
10658590	Not Issued	30		Method and apparatus for double data rate serial ATA phy interface	KIM, OOK
10781405	Not Issued	41	02/18/2004	Cable with circuitry for asserting stored cable data or other information to an external device or user	KIM, OOK
10794015	Not Issued	30		Method and circuit for adaptive equalization of multiple signals in response to a control signal generated from one of the equalized signals	KIM, OOK
10974473	Not Issued	160	10/26/2004	System and method for multiple-phase clock generation	KIM, OOK
60243855	Not Issued	159	10/27/2000	Clock and data recovery scheme using multiple-phase clocks and a barrel shifter	KIM, OOK
60276672	Not Issued	159	03/16/2001	Encoding scheme for a phase modulated clock signal	KIM, OOK

60277219	Not Issued	159	03/19/2001	Encoding scheme for a phase modulated clock signal	KIM, OOK
60282672	Not Issued	159	04/09/2001	Multi-phase VCO with common mode control	KIM, OOK
<u>60296653</u>	Not Issued	159	06/06/2001	Clock divider scheme for multiple-phase clock generation	KIM, OOK
60296655	Not Issued	159	06/06/2001	Method and apparatus for adaptive control of PLL loop bandwidth	KIM, OOK
60296664	Not Issued	159	06/06/2001	High-speed bus with embedded clock signals	KIM, OOK
<u>60406858</u>	Not Issued	159	08/29/2002	CMOS transceiver with dual current path VCO	KIM, OOK
60409367	Not Issued	159	09/06/2002	Methods and apparatus for double data rate serial ATA PHY interface	KIM, OOK
10875967	Not Issued	30	06/24/2004	Photoresist coating failure sensing methods and detection devices	KIM, OOK HYUN
11254296	Not Issued	30	10/19/2005	Wafer edge exposure method in semiconductor photolithographic processes, and orientation flatness detecting system provided with a WEE apparatus	KIM, OOK HYUN

 $Inventor\ Search\ Completed:\ No\ Records\ to\ Display.$

		Last Name		First Name	
Search Another:	Inventor	kim	ook		Search

To go back use Back button on your browser toolbar.



Day: Thursday Date: 1/5/2006 Time: 16:54:16

Inventor Name Search Result

Your Search was:

Last Name = KIM First Name = MIN-KYU

Application#	Patent#	Status		Title	Inventor Name
08392344	<u>5786860</u>	150	02/22/1995	HIGH SPEED BLOCK MATCHING FOR BI- DIRECTIONAL MOTION VECTOR ESTIMATION	KIM, MIN-KYU
<u>09291745</u>	Not Issued	161	04/14/1999	METHOD OF SELECTING OPTIMUM VALUE OF INPUT VARIABLE	KIM, MIN-KYU
09393235	6463092	150	09/09/1999	SYSTEM AND METHOD FOR SENDING AND RECEIVING DATA SIGNALS OVER A CLOCK SIGNAL LINE	KIM, MIN-KYU
09709816	Not Issued	161	11/10/2000	System and method for sending and receiving data signals over a clock signal line	KIM, MIN-KYU
09881271	<u>6954491</u>	150	06/14/2001	METHODS AND SYSTEMS FOR SENDING SIDE- CHANNEL DATA DURING DATA INACTIVE PERIOD	KIM, MIN-KYU
09905511	Not Issued	71	07/13/2001	Method and apparatus for adaptive control of PLL loop bandwidth	KIM, MIN-KYU
09905615	6944804	150	07/13/2001	SYSTEM AND METHOD FOR MEASURING PSEUDO PIXEL ERROR RATE	KIM, MIN-KYU
09953703	Not Issued	161	09/12/2001	Method and apparatus for encrypting auxiliary data transmitted over a TMDS-like link	KIM, MIN-KYU
<u>09954291</u>	Not Issued	30	09/12/2001	Method and apparatus for synchronizing auxiliary data and video data transmitted over a TMDS-like link	KIM, MIN-KYU
09954663	Not Issued	41	09/12/2001	Method and apparatus for sending auxiliary data on a TMDS-like link	KIM, MIN-KYU
09989487	<u>6985005</u>	150	11/20/2001	DIFFERENTIAL AMPLIFIERS USING ASYMMETRIC TRANSFER CHARACTERISTICS TO SUPPRESS INPUT NOISE IN OUTPUT LOGIC SIGNALS	KIM, MIN-KYU
09989580	Not Issued	61	11/20/2001	Bidirectional bridge circuit having high common mode rejection and high input sensitivity	KIM, MIN-KYU
09989647	Not Issued	90	11/20/2001	LOGIC GATES INCLUDING DIODE-CONNECTED METAL-OXIDE-SEMICONDUCTOR FIELD-EFFECT TRANSISTORS (MOSFETS) TO CONTROL INPUT THRESHOLD VOLTAGE LEVELS AND SWITCHING TRANSIENTS OF OUTPUT LOGIC SIGNALS	KIM, MIN-KYU
10024299	6693827	150	12/21/2001	MEMORY CELL SENSING CIRCUIT	KIM, MIN-KYU
10099533	Not Issued	71	03/15/2002	Combining a clock signal and a data signal	KIM, MIN-KYU
10247551	<u>6982691</u>	150	09/20/2002	METHOD OF DRIVING CHOLESTERIC LIQUID CRYSTAL DISPLAY PANEL FOR ACCURATE GRAY- SCALE DISPLAY	KIM, MIN-KYU
10315733	<u>6741321</u>	150	12/09/2002	RAW PANEL FOR LIQUID CRYSTAL DISPLAY AND MANUFACTURING METHOD FOR COLOR LIQUID CRYSTAL DISPLAY USING RAW PANEL	KIM, MIN-KYU
10371220	Not Issued	30	02/19/2003	Data synchronization across an asynchronous boundary using, for example, multi-phase clocks	KIM, MIN-KYU
10473259	Not Issued	93	09/29/2003	AZIRIDINE DERIVATIVES AND THEIR PREPARATION METHODS	KIM, MIN-KYU
10835301	<u>6897793</u>	150	04/29/2004	METHOD AND APPARATUS FOR RUN LENGTH LIMITED TMDS-LIKE ENCODING OF DATA	KIM, MIN-KYU
10915221	Not Issued	30	08/10/2004	Memory controller that selectively changes frequency of a memory clock signal, a smart card including the same, and a method of controlling a read operation of a memory	KIM, MIN-KYU

11188055	Not Issued	20	07/22/2005	Semiconductor device that initializes memory cells of an activated wordline group	KIM, MIN-KYU
11263058	Not Issued	30	10/31/2005	Method of manufacturing a thin film transistor, a thin film transistor manufactured by the method, a method of manufacturing flat panel display device, and a flat panel display device manufactured by the method	KIM, MIN-KYU
11264303	Not Issued	19	10/31/2005	Clock-edge modulated serial link with DC-balance control	KIM, MIN-KYU
60296654	Not Issued	159	06/06/2001	Pseudo pixel error detector for TMDS	KIM, MIN-KYU
60296655	Not Issued	159	06/06/2001	Method and apparatus for adaptive control of PLL loop bandwidth	KIM, MIN-KYU
10542920	Not Issued	19	01/01/0001	Apparatus and method for inputting alphabet characters	KIM, MIN-KYUM
11220065	Not Issued	20	09/06/2005	Apparatus and method for inputting alphabet characters on keypad	KIM, MIN-KYUM
11220091	Not Issued	20	09/06/2005	Apparatus and method for inputting alphabet characters on keypad	KIM, MIN-KYUM
10018333	Not Issued	41		Apparatus and method for inputting alphabet characters on small keypad	KIM, MIN-KYUM MIN

Inventor Search Completed: No Records to Display.

	Last Name	First Name	
Search Another: Inventor		min-kyu	Search
	{\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	Į i i i i i i i i i i i i i i i i i i i	

To go back use Back button on your browser toolbar.



Day: Thursday Date: 1/5/2006 Time: 16:53:32

Inventor Name Search Result

Your Search was:

Last Name = KIM First Name = GYUDONG

Application#	Patent#	Status			Inventor Name
08815486	6157360	150	03/11/1997	SYSTEM AND METHOD FOR DRIVING COLUMNS OF AN ACTIVE MATRIX DISPLAY	KIM, GYUDONG
09148583	<u>6271816</u>	150	09/04/1998	POWER SAVING CIRCUIT AND METHOD FOR DRIVING AN ACTIVE MATRIX DISPLAY	KIM, GYUDONG
09148815	6144242	150	09/04/1998	CONTROLLABLE DELAYS IN MULTIPLE SYNCHRONIZED SIGNALS FOR REDUCED ELECTROMAGNETIC INTERFERENCE AT PEAK FREQUENCIES	KIM, GYUDONG! ()f'
09234777	6600771	150	01/20/1999	SPREAD SPECTRUM PHASE MODULATION FOR SUPPRESSION OF ELECTROMAGNETIC INTERFERENCE IN PARALLEL DATA CHANNELS	KIM, GYUDONG
09393235	6463092	150	09/09/1999	SYSTEM AND METHOD FOR SENDING AND RECEIVING DATA SIGNALS OVER A CLOCK SIGNAL LINE	KIM, GYUDONG
09579811	6870930	150	05/26/2000	METHODS AND SYSTEMS FOR TMDS ENCRYPTION	KIM, GYUDONG
09709816	Not Issued	161	11/10/2000	System and method for sending and receiving data signals over a clock signal line	KIM, GYUDONG
09881271	6954491	150	06/14/2001	METHODS AND SYSTEMS FOR SENDING SIDE- CHANNEL DATA DURING DATA INACTIVE PERIOD	KIM, GYUDONG
09905511	Not Issued	71	07/13/2001	Method and apparatus for adaptive control of PLL loop bandwidth	KIM, GYUDONG
09905615	6944804	150	07/13/2001	SYSTEM AND METHOD FOR MEASURING PSEUDO PIXEL ERROR RATE	KIM, GYUDONG
09922990	6961095	150	08/03/2001	DIGITAL DISPLAY JITTER CORRECTION APPARATUS AND METHOD	KIM, GYUDONG
09953703	Not Issued	161	09/12/2001	Method and apparatus for encrypting auxiliary data transmitted over a TMDS-like link	KIM, GYUDONG
09954291	Not Issued	30	09/12/2001	Method and apparatus for synchronizing auxiliary data and video data transmitted over a TMDS-like link	KIM, GYUDONG
09954663	Not Issued	41	09/12/2001	Method and apparatus for sending auxiliary data on a TMDS-like link	KIM, GYUDONG
09989487	<u>6985005</u>	150	11/20/2001	DIFFERENTIAL AMPLIFIERS USING ASYMMETRIC TRANSFER CHARACTERISTICS TO SUPPRESS INPUT NOISE IN OUTPUT LOGIC SIGNALS	KIM, GYUDONG
09989580	Not Issued	61	11/20/2001	Bidirectional bridge circuit having high common mode rejection and high input sensitivity	KIM, GYUDONG
09989587	6717478	150	11/20/2001	MULTI-PHASE VOLTAGE CONTROLLED OSCILLATOR (VCO) WITH COMMON MODE CONTROL	KIM, GYUDONG
09989645	6809567	150	11/20/2001	SYSTEM AND METHOD FOR MULTIPLE-PHASE CLOCK GENERATION	KIM, GYUDONG
09989647	Not Issued	90	11/20/2001	LOGIC GATES INCLUDING DIODE-CONNECTED METAL-OXIDE-SEMICONDUCTOR FIELD-EFFECT TRANSISTORS (MOSFETS) TO CONTROL INPUT THRESHOLD VOLTAGE LEVELS AND SWITCHING TRANSIENTS OF OUTPUT LOGIC SIGNALS	KIM, GYUDONG
10099533	Not Issued	71	03/15/2002	Combining a clock signal and a data signal	KIM, GYUDONG
10371220	Not Issued	30	02/19/2003	Data synchronization across an asynchronous boundary using,	KIM, GYUDONG

i.			•		
				for example, multi-phase clocks	
10439446	Not Issued	93	05/16/2003	METHOD AND CIRCUIT FOR DC MEASUREMENT OF PHASE UNIFORMITY OF MULTI-PHASE CLOCKS	KIM, GYUDONG
<u>10763905</u>	Not Issued	30	01/23/2004	Reduced dead-cycle, adaptive phase tracking method and apparatus	KIM, GYUDONG
<u>10781405</u>	Not Issued	41	02/18/2004	Cable with circuitry for asserting stored cable data or other information to an external device or user	KIM, GYUDONG
10794015	Not Issued	30	03/05/2004	Method and circuit for adaptive equalization of multiple signals in response to a control signal generated from one of the equalized signals	KIM, GYUDONG
<u>10835301</u>	<u>6897793</u>	150	04/29/2004	METHOD AND APPARATUS FOR RUN LENGTH LIMITED TMDS-LIKE ENCODING OF DATA	KIM, GYUDONG
10842231	Not Issued	30	05/10/2004	Data sampling method and apparatus using through-transition counts to reject worst sampling position	KIM, GYUDONG
11219323	Not Issued	20	09/01/2005	Digital display jitter correction apparatus and method	KIM, GYUDONG
11264303	Not Issued	19	10/31/2005	Clock-edge modulated serial link with DC-balance control	KIM, GYUDONG
60058041	Not Issued	159	09/04/1997	DATA DRIVING WITH REDUCED EMI	KIM, GYUDONG
60058042	Not Issued	159	09/04/1997	POWER SAVING METHODS FOR COLUMN DRIVERS	KIM, GYUDONG
60071805	Not Issued	159	01/20/1998	SUPPRESSION OF ELECTROMAGNETIC INTERFERENCE IN PARALLEL DATA CHANNELS THROUGH SPREAD SPECTRUM PHASE MODULATION	KIM, GYUDONG
60071879	Not Issued	159	01/20/1998	1.25GBAUD CMOS DRIVER AND ON-CHIP TERMINATION FOR GIGABIT ETHERNET PHY CHIP	KIM, GYUDONG
60099770	Not Issued	159	09/10/1998	EMBEDDED BACK CHANNEL FOR TMDS	KIM, GYUDONG
60117488	Not Issued	159	01/27/1999	ENCRYPTION METHOD FOR TMDS	KIM, GYUDONG
60136695	Not Issued	159	05/28/1999	ENCRYPTION METHOD FOR TMDS	KIM, GYUDONG
60222859	Not Issued	159	08/04/2000	Scalable digital display controller dejitter circuit	KIM, GYUDONG
60276672	Not Issued	159	03/16/2001	Encoding scheme for a phase modulated clock signal	KIM, GYUDONG
60277219	Not Issued	159	03/19/2001	Encoding scheme for a phase modulated clock signal	KIM, GYUDONG
60282672	Not Issued	159	04/09/2001	Multi-phase VCO with common mode control	KIM, GYUDONG
60296653	Not Issued	159	06/06/2001	Clock divider scheme for multiple-phase clock generation	KIM, GYUDONG
60296654	Not Issued	159	06/06/2001	Pseudo pixel error detector for TMDS	KIM, GYUDONG
60296655	Not Issued	159	06/06/2001	Method and apparatus for adaptive control of PLL loop bandwidth	KIM, GYUDONG
60475032	Not Issued	159	06/02/2003	Data sampling method and apparatus using through-transition counts to reject worst sampling position	KIM, GYUDONG

Inventor Search Completed: No Records to Display.

	Last Name	First Name	
Search Another: Inventor	kim	gyudong	Search

To go back use Back button on your browser toolbar.